

## NEW SHEET

500

## **ENTER**

5/0

540

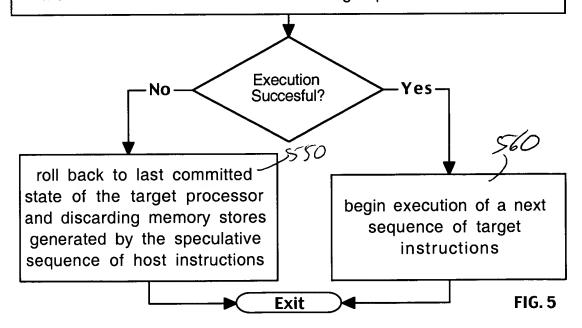
*5*20

begin execution of a first sequence of target instructions by committing state of the target processor and storing memory stores generated by previously-executed sequences of instructions at a point in the execution of instructions at which state of the target processor is known

begin execution of a speculative sequence of host instructions following a branch from the first sequence of target instructions by immediately committing state and storing memory stores

release a lock if the speculative sequence of host instructions is running in a locked condition

attempt to execute the speculative sequence of host instructions until another point in the execution of target instructions at which state of the target processor is known



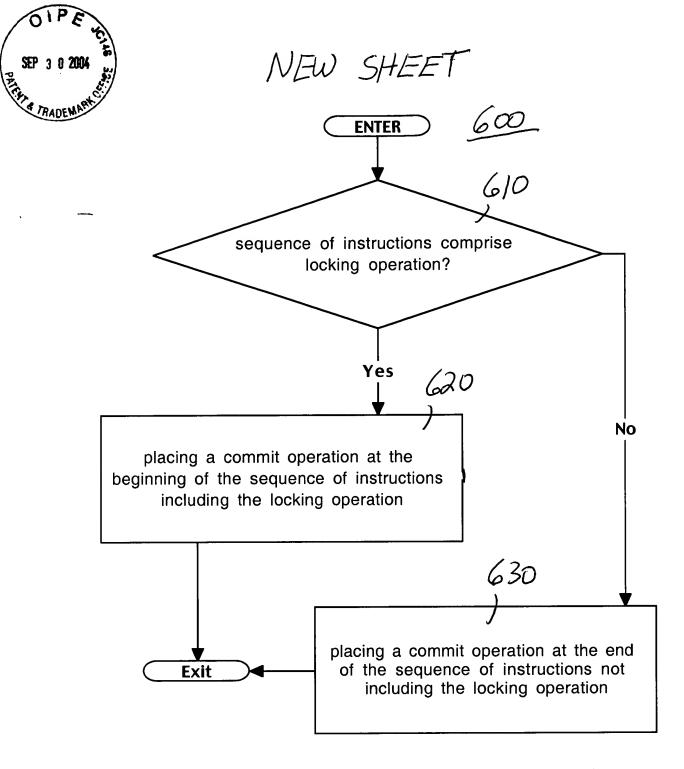


FIG. 6